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Amendments to the Claims:

Amend the claim set, replacing all prior versions, without prejudice or disclaimer of the subject matter thereof, as detailed in the following complete listing of all claims:

1. (Original) A validation protocol for determining whether an untrusted authentication chip is valid, or not, including the steps of:

generating a random number and encrypting it with an asymmetric encryption function using a first key;

passing the encrypted random number to an untrusted authentication chip;

decrypting the encrypted random number with an asymmetric decryption function using a secret key, in the untrusted authentication chip;

comparing the decrypted random number with the original random number, and in the event of a match considering the untrusted chip to be valid;

otherwise considering the untrusted chip to be invalid.

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- 2. (Original) A validation protocol according to claim 1, where the random number is not secret, but where the trusted authentication chip contains a random function to produce random numbers from a seed, and the function advances after every random number is produced so that the next random number will be produced from a new seed.
- 3. (Original) A validation protocol according to claim 1, where the first key is a public key.
- 4. (Original) A validation protocol according to claim 1, where the encryption is implemented in software.
- 5. (Original) A validation protocol according to claim 1, where the encryption is implemented in a second authentication chip.
- 6. (Original) A validation protocol according to claim 1, where the keys used for encryption and decryption are 2048 bits or larger.
- 7. (Currently Amended) A validation system for performing the method according to claim 1 determining whether an untrusted authentication chip is valid or not, where the system

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includes comprises:

a random number generator, to generate an original random number;
an asymmetric encryptor to encrypt generated random numbers and a first key for the
encryptor , and ;
an untrusted authentication chip which receives the encrypted random number; the
untrusted chip includinges an asymmetric decryption function to decrypt encrypted random
numbers and a secret key for the decryption function; and
a-comparison means are also provided to compare athe decrypted random number with an
the original random number;
whereby, and in the event of a match between the decrypted random number and the
original random number, considering the untrusted chip is considered to be valid; otherwise
eonsidering the untrusted chip is considered to be invalid.

- 8. (Original) A validation system according to claim 7, where the random number generator, encryptor and comparison means are in an external system.
- 9. (Original) A validation system according to claim 8, where the external system is in a device in which are mounted, and the untrusted chip is in the consumable.
- 10. (Original) A validation system according to claim 7, where the random number generator and encryptor are in a second authentication chip, and the comparison means are in an external system which receives the random number and the encrypted version before passing only the encrypted version to the untrusted chip; the system also receives back the decrypted version from the untrusted chip and performs the comparison.
- 11. (Original) A validation system according to claim 10, where the system is in a device in which consumables are mounted, and the untrusted chip is in the consumable.
- 12. (Original) A validation system according to claim 7, where the random number is not secret, but the random number generator includes a random function to produce random numbers from a seed, and the function advances after every random number is produced so that the next random number will be produced from a new seed.

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- 13. (Original) A validation system according to claim 7, where the first key is a public key.
- 14. (Original) A validation system according to claim 7, where the encryption is implemented in software.

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- 15. (Original) A validation system according to claim 7, where the encryption is implemented in a second authentication chip.
- 16. (Original) A validation system according to claim 7, where the keys used for encryption and decryption are 2048 bits or larger.